however, always check jumper positions on actual boards since there is no guarantee that all jumpers are in default place. Default jumper settings are indicated in the schematic.
Note: 1V8 rail is actually 1.4V since internal regulator in default

Analog parts of LPC3154

Power supply parts of LPC3154

Not used parts of LPC3154

Production test connector
Boot mode - USB via DFU class

LPC3154-JTAG

45 ohm impedance routing, 90 ohm differential
No tracks within 10 times trace width
No vias and no sharp angles

LED

SPI-FLASH

Not mounted
J4 not mounted, but can be if bridge wires below are cut.

Bridge wires can be cut on pcb by removing solder paste.

J5 can connect to external SWD/JTAG interface if LPCXpresso target side (LPC17xx) is disconnected.
JP1 normally shorted. Can be used for current consumption measurements on U7.