Date: October 7, 2017

Summary of Lecture 11

Reading: [Arora-Barak (AB)] Chap 5.3, 6.1.

- Time-Space trade-off for solving the SAT problem. Proof of SAT \notin TISP $(n^{1.2}, n^{0.2})$.
- The motivation of studying the circuit model. The difference between the circuit model and Turing machines.
- Basic mathematical formulation of circuits: input, output, gates $(\mathcal{B}_0, \mathcal{B}_1)$, size, depth.