CMSC 430 (Spring 2009)
Practice Problems 4 Solutions

Use the following 3-address code and Java stack code instructions for answering code generation questions.

<table>
<thead>
<tr>
<th>3-addr Instruction</th>
<th>Effect</th>
</tr>
</thead>
<tbody>
<tr>
<td>load R1 x</td>
<td>R1 ← x</td>
</tr>
<tr>
<td>store x R1</td>
<td>x ← R1</td>
</tr>
<tr>
<td>add R1 R2 R3</td>
<td>R1 ← R2 + R3</td>
</tr>
<tr>
<td>sub R1 R2 R3</td>
<td>R1 ← R2 - R3</td>
</tr>
<tr>
<td>mult R1 R2 R3</td>
<td>R1 ← R2 * R3</td>
</tr>
<tr>
<td>neg R1 R2</td>
<td>R1 ← -(R2)</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Java Stack Code</th>
<th>Effect</th>
</tr>
</thead>
<tbody>
<tr>
<td>nop</td>
<td>none</td>
</tr>
<tr>
<td>ldc_int c</td>
<td>push constant c onto stack</td>
</tr>
<tr>
<td>iload index(x)</td>
<td>push local variable X onto stack</td>
</tr>
<tr>
<td>istore index(x)</td>
<td>pop stack, store in local variable X</td>
</tr>
<tr>
<td>iadd</td>
<td>pop 2 elems off stack, add, push</td>
</tr>
<tr>
<td>isub</td>
<td>pop 2 elems off stack, subtract, push</td>
</tr>
<tr>
<td>imult</td>
<td>pop 2 elems off stack, multiply, push</td>
</tr>
<tr>
<td>neg</td>
<td>pop stack, negate, push</td>
</tr>
<tr>
<td>goto L</td>
<td>jump to handle L</td>
</tr>
<tr>
<td>ifeq L</td>
<td>pop stack, jump to handle L if zero</td>
</tr>
<tr>
<td>if_icmpeq L</td>
<td>pop 2 elems, jump to L if equal</td>
</tr>
<tr>
<td>if_icmpgt L</td>
<td>pop 2 elems, jump to L if greater</td>
</tr>
<tr>
<td>dup</td>
<td>duplicate top of stack</td>
</tr>
<tr>
<td>pop</td>
<td>pop top of stack</td>
</tr>
<tr>
<td>swap</td>
<td>swap top two positions of stack</td>
</tr>
</tbody>
</table>

1. Run-time environment

(a) What is a frame used for?
   A frame (activation record) stores run-time information needed to maintain the environment of a function during program execution.

(b) Name six items of run-time information stored in a frame. For each item, identify whether its value is set before the procedure is called, during procedure execution, or right before procedure return.
   - parameters - set before invocation
   - return value - set right before procedure return
   - return address - set before invocation
   - pointer to frame of enclosing function (access link) - set before invocation
   - pointer to frame of calling function - set before invocation
   - local variables - set during procedure execution

(c) Name one type additional type of information only stored in a frame for Java programs.
   Local stack (for stack operations)

(d) When can storage for a variable be allocated in a frame?
   When the variable has fixed size and is no longer accessible after procedure return.

(e) What advantage is obtained when allocating variables in a frame?
   Entire frame may be freed at together at once when returning from procedure.

(f) Name two advantages of managing memory allocation manually in the code.
   Most efficient (least wasted memory, memory freed at earliest opportunity), little run-time overhead.

(g) Name two advantages of managing memory allocation automatically in the run-time system.
   Less programmer effort, possibility of premature freeing of memory greatly reduced.

(h) Name two methods of managing memory allocation automatically in the run-time system.
   Reference counting keeps track of number of possible references, freeing when count reaches zero.
   Mark-and-scan looks for accessible memory during program execution and frees inaccessible memory.

2. Intermediate representations.

Consider the statements:
- \( x := a + (b \times a) \)
- \( x := a - ((b + a) \times c) \)

(a) Translate each into an AST
   
   \[ := \]
   \[ / \]
   \[ / \]
   \[ \]
   \[ \]
   \[ a \times a \]
   \[ / \]
   \[ / \]
   \[ b \times a \]
   
   (b) Translate each into 3-address code
   
   ```
   load R1 a
   load R2 b
   load R3 c
   mult R4 R2 R3
   add R5 R1 R4
   store R5 R6
   ```

   (c) Translate each into Java stack code
   
   ```
   iload index(a)
   iload index(b)
   iload index(c)
   iadd
   imult
   istore index(x)
   ```
(d) Which representation is the most compact? Why?

Java byte code is the most compact, because it can take advantage of the implicit stack to avoid naming its operands for many instructions.

(e) Which representation is easy to manipulate? Why?

ASTs are most easy to manipulate for high-level code, 3-address code is easy to manipulate for low-level code.

(f) Which representation is hard to manipulate? Why?

Java byte code is hard to manipulate because stack operations are more difficult to reorder.

(g) Which representation is closest to the input program? Why?

ASTs are closest to the input program because it maintains the original grammatical structure.


You are generating code for a Java stack machine. You are given the following grammar attributes and helper functions:

<table>
<thead>
<tr>
<th>Attribute</th>
<th>Holds</th>
</tr>
</thead>
<tbody>
<tr>
<td>AstNode.code</td>
<td>list of instructions</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Function</th>
<th>Effect</th>
</tr>
</thead>
<tbody>
<tr>
<td>genInst( X )</td>
<td>create new instruction X</td>
</tr>
<tr>
<td>append( ... )</td>
<td>returns handle to instruction</td>
</tr>
<tr>
<td></td>
<td>concatenates lists of instructions</td>
</tr>
</tbody>
</table>

(a) What grammar actions needed to generate code for a C-style IF statement in the following production?

```
stmt → IF (exp) stmtList ;
{ stmt.code = ??; }
```

Answer:
```
stmt := IF (exp) stmtList ;
{
    Handle h1 = genInst( NOP );
    stmt.code = append (exp.code,
                        genInst( IFEQ( h1 ) ),
                        stmtList.code,
                        h1);
}
```

(b) What grammar actions needed to generate code for a C-style FOR loop in the following production?

```
stmt → FOR (stmt ; exp ; stmt) stmt ;
{ stmt.code = ??; }
```

Answer:
```
stmt := FOR (stmt1 ; exp ; stmt2) stmt3 ;
{
    Handle h1 = genInst( NOP );
    Handle h2 = genInst( NOP );
    stmt.code = append (stmt1.code,
                        h1, exp.code,
                        genInst( IFEQ( h2 ) ),
                        stmt3.code,
                        stmt2.code,
                        genInst( GOTO( h1 ) ),
                        h2);
}
```

(c) Write grammar actions needed to generate control code for an AND expression in the following production, using numerical value representation of booleans. Use short circuiting.

```
exp → exp1 AND exp2
{ exp.code = ??; }
```

Answer:
```
exp := exp1 AND exp2
{
    Handle h1 = genInst( NOP );
    exp.code = append (exp1.code,
                        dup,
                        genInst( IFEQ( h1 ) ),
                        pop,
                        exp2.code,
                        h1);
}
```

(d) Write grammar actions needed to generate control code for an NOR expression in the following production, using numerical value representation of booleans. Use short circuiting.

```
exp → exp1 NOR exp2
{ exp.code = ??; }
```

Answer:
```
exp := exp1 NOR exp2
{
    Handle h1 = genInst( NOP );
    Handle h2 = genInst( NOP );
    Handle h3 = genInst( NOP );
    Handle h4 = genInst( NOP );
    exp.code = append (exp1.code,
                        genInst( IFEQ( h1 ) ),
                        genInst( GOTO( h2 ) ),
                        h1,
                        exp2.code,
                        genInst( IFEQ( h3 ) ),
                        h2,
                        genInst( LDC_INT( 0 ) ),
                        genInst( GOTO( h4 ) ),
                        h3,
                        genInst( LDC_INT( 1 ) ),
                        h4);
}
```
(e) Write grammar actions needed to generate control code for an \( \geq \) (GEQ) expression in the following production, using numerical value representation of booleans.

\[
\text{exp} \rightarrow \text{exp}_1 \text{GEQ} \text{exp}_2
\]

\[
\{ \text{exp.code} = ??; \}
\]

Answer:

\[
\text{exp} := \text{exp}_1 \geq \text{exp}_2
\]

\[
\{ \text{Handle h1 = genInst( NOP );}
\]

\[
\text{Handle h2 = genInst( NOP );}
\]

\[
\text{exp.code} = \text{append (}
\]

\[
\text{exp1.code, }
\]

\[
\text{exp2.code,}
\]

\[
\text{genInst( IF_ICMPEQ( h1 ) ), }
\]

\[
\text{exp1.code, }
\]

\[
\text{exp2.code,}
\]

\[
\text{genInst( IF_ICMPGT( h1 ) ),}
\]

\[
\text{genInst( LDC_INT( 0 ) ),}
\]

\[
\text{genInst( GOT0( h2 ) ),}
\]

\[
h1,
\]

\[
\text{genInst( LDC_INT( 1 ) ),}
\]

\[
h2
\]

\[
\}
\]

4. Complex code generation.

(a) Name two issues and solutions to generating code for function calls in C.

Function call could occur in an expression (e.g., \( 4 + \text{foo}() \)), need to calculate value of function and use return value in expression

Function argument could be an expression (e.g., \( \text{foo}(4+x) \)), need to evaluate expression first and pass as argument

(b) Name two issues and solutions to generating code for array references in C.

Could have multidimensional arrays, need to convert indices into memory location by flattening the array in either row or column major order

Could have arrays or array elements as arguments (e.g., \( \text{foo(a[]}, \text{foo(a[4])} \)), need to decide whether to pass value or address depending on whether call-by-reference or call-by-value

(c) What code must the compiler generate for the code

\[
\text{int i, a[100];}
\]

\[
\ldots
\]

\[
a[i + 5] = 4;
\]

For \( a[i+5] = 4 \), compiler must generate code to calculate \( i+5 \), (adjusting by first array index, usually 0 or 1) multiply result by size of elements of array, then add to base address of \( a \) to find address of array element

(d) What code must the compiler generate for the code

\[
\text{int foo ( int x );}
\]

\[
\ldots
\]

\[
x = \text{foo} ( i + 2 );
\]

i. Assuming foo() is call-by-value?

If foo() is call-by-value, calculate \( i+2 \) and use value as argument

ii. Assuming foo() is call-by-reference?

If foo() is call-by-reference, calculate \( i+2 \), store in temporary variable, then pass address of temp var as argument