

# Automating Redesign of Electro-Mechanical Assemblies

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## Abstract

Automating redesign of electro-mechanical artifacts presents many difficult problems to the developers of intelligent CAD systems. To address this need requires we identify new AI technologies for representation and planning and effectively apply them to the manufacturing problem. In this paper, we present the redesign problem and outline some of the critical points; presenting how to bring existing AI research to bear in creating more effective CAD tools.

## 1 Introduction

Computer-aided design (CAD) and CAD software is fast becoming a ubiquitous component of the modern manufacturing workplace. The decreasing cost of computational power has made sophisticated software for tasks such as finite element and mechanism analysis essential for increasing engineering quality and productivity. Software tools designed to reduce time-consuming *build-test-redesign* iterations are becoming crucial components for supporting concurrent engineering.

Many of these are tools for design analysis and critiquing. For example, they might examine whether a candidate design violates manufacturing or functional constraints (such as stress, acceleration, and so forth); or they might attempt to find possible suggestions to the user about how to improve a design [12, 10, 16, 3, 21]. Other analysis tools might include those that help the designer foresee potential problems with product life-cycle considerations such as performance, producibility, reliability, maintainability, and so forth.

These design analysis and critiquing systems, in order to realize the advantages of collaborative engineering, must consider downstream manufacturing and life-cycle activities during the design phase. This has stretched the limits of traditional design activities and increased their complexity—presenting a variety of difficult computational problems such as the following:

- how to represent partial or incomplete design information;

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- how to reason with partial or incomplete design information;
- how to access and intelligently reuse legacy information (for example, in a corporate knowledge base);
- how to mediate conflicts to satisfy contradictory manufacturing constraints;
- how to provide quick responses for interactive computing environments.

The automated redesign problem cuts across all of these issues and is of increasing interest to researchers, in both academia and industry. While some commercial software tools exist (such as those to reduce the number of parts in an assembly), satisfying solutions to the general redesign problem have eluded researchers. Existing systems vary significantly by approach, scope, and level of sophistication, with most attempting to capture manufacturability problems as collections of rules or heuristics. However, it has proven difficult to capture subtle manufacturability problems with hard-coded and coarse rules. Many problems can only be detected at the manufacturing planning level; problems that are compounded when multiple artifacts interact, not only in assemblies, but across the manufacturing enterprise. As a further complication, design is an interactive process and thus all of these computations must be handled in real-time.

By building on our previous work in manufacturing and [15, 14, 20, 2, 13, 11] AI [18, 32, 5, 23, 27, 28, 7, 9, 17], we will identify promising new AI technologies for enabling redesign and outline how they may be effectively applied to the manufacturing problem. We anticipate that this work will serve to further the development of redesign systems by both expanding and improving the application of AI technologies to the problem; leading to the development of systematic methodologies for automated redesign. This will speed the introduction of automated designer's aides that capable of simultaneously considering design goals and manufacturing constraints, and identifying and alleviating manufacturing problems during the design stage.

## 2 Scenarios for Intelligent Automated Redesign

There are a variety of scenarios in which redesign is a necessary step in the product development cycle:

**Changes in Functional Requirements.** Consider the situation where the functional requirements of a new design have minor differences from those of an existing design. For example, modifying the design of an existing engine housing to accommodate a larger motor.

**Incorporation of new manufacturing technology.** In order to take full advantage of a new manufacturing process, existing products may require redesign. For example, engine blocks have traditionally been manufactured using a casting that is later machined. As die casting becomes increasingly economical, coupled with the pressure to construct lighter, more fuel-efficient automobiles, designers must plan for the possibility of die-casted engine blocks. While the function of the engine block will be identical, redesign will need to adjust existing engine block designs to suit the die casting process.

**Changes to production resources.** Manufacturing facilities for an organization will change over time and may often need to be rapidly reconfigured to adjust to abrupt changes in market forces. Other considerations that redesign may have to accommodate include equipment failure and the addition of new facilities and tools.

**Redesign for improved manufacturability.** During the design process, a design often goes through a cycle of analysis and review during which cost effectiveness and quality are constantly evaluated. Unfortunately, however, in many situations beneficial design modifications are discovered by experienced process planners and machinists only after the component enters the production cycle. Making automated tools for suggesting design revisions available during the design stage would reduce the product realization cost [2].

Ideally, an interactive and highly-automated redesign system will need to be capable of analyzing the artifact's design history, its relationship to similar parts in a company's corporate manufacturing knowledge bases or files, and the constraints imposed by the different interacting design and manufacturing teams working concurrently on the product.

### 3 Three Challenges

To enhance the intelligence and function of the next generation of redesign systems, we outline three focus areas that have potential for positive impact.

#### 3.1 Plan Merging and Reuse

Consider the case where a design has one or more process plans associated with it. If the design gets modified, the brute force generative approach is to re-plan from scratch. In order to adjust plans based on design changes, techniques for plan reuse and plan merging [31, 30, 29, 18, 32] might be applied to isolate portion of the design that requires re-planning. Further, one might be able to adjust plans to accommodate changes in manufacturing process and production resources.

A different, but compatible approach, is to view design as an incremental process [5, 4] and modeled as an incremental planning task. As design information (such as tolerances, surface finish requirements, functional specifications and associations, relationships to manufacturing processes) is modified, these changes can be translated into modifications to process and production plan.

#### 3.2 Reactive and Impartial Information Planning

Engineering design and manufacturing planning each are executed concurrently at several different levels of abstraction. For instance, design proceeds from conceptual level, through embodiment, eventually yielding a detailed design of the product. Similarly, manufacturing planning is done for individual machines, the level of the factory, and enterprise wide. At many stages of these processes, planning proceeds without having access to all of the relevant information.

Planning techniques for imperfect information domains [23, 22, 25, 24] can be used to represent partially completed designs and reason about their intermediate process plans.

In order to handle simultaneous planning at multiple levels of abstraction, work on architectures for reactive systems can be adapted to address the different types of goals that arise when performing initial process and production planning activities [26, 27].

#### 3.3 High-Performance AI

Design is a highly interactive process. Hence, predicting characteristics such as design performance and manufacturability (i.e., product cost, quality, and lead time) ideally must be done in real-time. As the cost of high-performance machines and distributed networks of computers continues to drop, we need to carefully examine how to migrate applications to these architectures. In particular, methods for executing multi-processor search and knowledge queries can be directly applied to key problems in manufacturing planning:

**Search.** In the University of Maryland's Interactive Manufacturability Analysis and Critiquing System (IMACS) project [19, 15, 14], branch and bound search strategy to navigate the space of possible machining plans. IMACS generates and evaluations alternative plans in order to estimate manufacturing cost and calculate an optimal plan. Application of parallel search techniques [6, 28, 7] can lead to large performance increases for complex operation planning problems and for interactive applications.

**Knowledge Management.** Parallel search and knowledge representations can be applied to the searching of very large knowledge-bases of corporate design and manufacturing information (e.g. given a design's functional specifications, find similar parts and process plans from the company database) [1, 8, 9, 17]

### 4 Conclusion

As we move toward greater levels of automation in computer-aided engineering environments, greater amounts of information can be captured and reused. One of the areas with great potential is automated redesign.

## References

- [1] William A. Andersen, James A. Hendler, Matthew P. Evett, and Brian P. Kettler. Massively parallel matching of knowledge structures. In Hiroaki Kitano and James Hendler, editors, *Massively Parallel Artificial Intelligence*, page in press. AAAI Press/The MIT Press, Menlo Park, California, 1994.
- [2] D. Das, S. K. Gupta, and D. Nau. Reducing setup cost by automated generation of redesign suggestions. In *ASME Conf. Computers in Engineering*, 1994. Best-paper award winner.
- [3] R. Dighe, M. J. Jakiela, and D. R. Wallace. Structural synthesis under manufacturability constraints: A CAD system for the design of injection-molded product housings. *Research in Engineering Design*, 5(3,4):185–201, 1993.
- [4] D. P. Eshner, J. Hendler, and D. S. Nau. Incremental planning using conceptual graphs. In *Proc. Fifth Workshop on Conceptual Graphs*, 1991.
- [5] D. P. Eshner, J. Hendler, and D. S. Nau. Incremental planning using conceptual graphs. *Journal of Experimental and Theoretical AI*, 4:85–94, 1992.
- [6] M. Evett, J. Hendler, A. Mahanti, and D. Nau. PRA\*: A memory-limited heuristic search procedure for the connection machine. In *Proc. Frontiers of Massively Parallel Computation*, October 1990.
- [7] M. Evett, J. Hendler, A. Mahanti, and D. Nau. PRA\*: Massively parallel heuristic search. *J. Parallel and Distributed Computing*, 1994. To appear.
- [8] Matthew P. Evett, James A. Hendler, and William A. Andersen. Massively parallel support for computationally effective recognition queries. In *Proceedings of the Eleventh National Conference on Artificial Intelligence*, pages 297–302, Menlo Park, California, 1993. AAAI Press.
- [9] Matthew P. Evett, James A. Hendler, and Lee Spector. Parallel knowledge representation on the Connection Machine. *Journal of Parallel and Distributed Computing*, 22:168–184, 1994.
- [10] R. Gadh, E.L. Gursoz, M.A. Hall, F.B. Prinz, and A.M. Sudhalkar. Feature abstraction in a knowledge-based critique of design. *Manufacturing Review*, 4(2):115–125, 1991.
- [11] S. K. Gupta and D. S. Nau. A systematic approach for analyzing the manufacturability of machined parts. *Computer Aided Design*, 1994. To appear.
- [12] S. K. Gupta and D. S. Nau. A systematic approach for analyzing the manufacturability of machined parts. *Computer Aided Design*, 1995. To appear.
- [13] S. K. Gupta, D. S. Nau, and G. Zhang. Concurrent evaluation of machinability during product design. *IEEE Computer*, 26(1):62–63, January 1993.
- [14] Satyandra K. Gupta, Thomas R. Kramer, Dana S. Nau, William C. Regli, and Guangming Zhang. Building MRSEV models for CAM applications. *Advances in Engineering Software*, 20(2/3):121–139, 1994.
- [15] Satyandra K. Gupta, William C. Regli, and Dana S. Nau. Integrating DFM with CAD through design critiquing. *Concurrent Engineering: Research and Applications*, 2(2), 1994.
- [16] M. Jakiela and P. Papalambros. Design and implementation of a prototype intelligent CAD system. *ASME Journal of Mechanisms, Transmission, and Automation in Design*, 111(2), June 1989.
- [17] Brian P. Kettler, James A. Hendler, William A. Andersen, and Matthew P. Evett. Massively parallel support for case-based planning. *IEEE Expert*, page in press, February 1994.

- [18] D. Nau, Q. Yang, and J. Hendler. Planning for multiple goals with limited interactions. In *Fifth IEEE Conference on Artificial Intelligence Applications*, 1989.
- [19] Dana S. Nau, Satyandra K. Gupta, Thomas R. Kramer, William C. Regli, and Guangming Zhang. Generation and evaluation of machining alternatives based on MRSEVs. In David G. Goldstein, editor, *AAAI SIGMAN Workshop on Intelligent Manufacturing Technology*, Menlo Park, CA 94025-3496, July 1993. American Association for Artificial Intelligence.
- [20] William C. Regli, Satyandra K. Gupta, and Dana S. Nau. Extracting alternative machining features: An algorithmic approach. *Research in Engineering Design*, 1995. To appear.
- [21] B.G. Silverman and T.M. Mezher. Expert critics in engineering design: Lessons learned and research needs. *AI magazine*, 13(1):45–62, 1992.
- [22] S. J. J. Smith and D. S. Nau. Strategic planning for imperfect-information games. In *Games: Planning and Learning, Papers from the 1993 Fall Symposium*, number Technical Report FS9302. AAAI Press, 1993.
- [23] S. J. J. Smith and D. S. Nau. An analysis of forward pruning. In *AAAI-94*, 1994.
- [24] S. J. J. Smith and D. S. Nau. A planning approach to declarer play in bridge. *Computational Intelligence*, 1995. Accepted subject to revisions; revisions are in progress.
- [25] S. J. J. Smith, D. S. Nau, and T. Throop. A hierarchical approach to strategic planning with non-cooperating agents under conditions of uncertainty. In *Proc. First Internat. Conf. AI Planning Systems*, pages 299–300, June 1992.
- [26] Lee Spector. Supervenience in dynamic-world planning. Technical Report CS-TR-2899, University of Maryland at College Park, Department of Computer Science, May 1992.
- [27] Lee Spector and James Hendler. Planning and control across supervenient levels of representation. In *Int'l Journal on Intelligent and Cooperative Information Systems*, volume 1, December 1992.
- [28] Q. Yang and D. Nau. Preprocessing search spaces for branch-and-bound search. In *IJCAI-89*, August 1989.
- [29] Q. Yang, D. Nau, and J. Hendler. An approach to multiple-goal planning with limited interactions. In *AAAI Spring Symposium*, Stanford, 1989.
- [30] Q. Yang and D. S. Nau. Optimization of multiple goal plans in automated manufacturing. In *Proceedings of the First SIGMAN Workshop on Manufacturing Planning*, Detroit, Michigan, August 1989.
- [31] Q. Yang, D. S. Nau, and J. Hendler. Optimization of multiple-goal plans with limited interaction. In *Proc. DARPA Workshop on Innovative Approaches to Planning, Scheduling and Control*, 1990.
- [32] Q. Yang, D. S. Nau, and J. Hendler. Merging separately generated plans with restricted interactions. *Computational Intelligence*, 8(2):648–676, February 1992.