Due at the start of class Wednesday, October 15, 2003.

**Problem 1.** Do Exercise 3.10 on page 199 of PH.

**Problem 2.** Do Exercise 4.14 on page 324 of PH.

**Problem 3.**
Translate the following MIPS assembly language instructions into MIPS machine language instructions:

- State the instruction type (R,I,J).
- Label each field (by name), and show the value of each field in binary.

(See page 140 for correspondence between register names and numbers.)

(a) nor $3, $26, $21
(b) subu $s3, $zero, $t3
(c) sw $s4, 110($t7)
(d) sli $12, $10, 4100
(e) bne $s2, $t9, OFFSET (Assume OFFSET is -50.)
(f) sltu $12, $10, $25
(g) j TARGET (Assume TARGET is 16400.)
(h) jr $ra