Today

- **Review Syllabus**
  - read the warning about the size of the project
- **Class Grades Server**
  - Grades.cs.umd.edu
- **Web Page**
- **Piazza**
  - [https://piazza.com/class/jzcrm1vz6j064b](https://piazza.com/class/jzcrm1vz6j064b)
- **Discussion Sections**
  - will focus on the project and meet twice a week
Catalog Description

- A hands-on introduction to operating systems, including topics in: multiprogramming, communication and synchronization, memory management, IO subsystems, and resource scheduling policies. The laboratory component consists of constructing a small kernel, including functions for device IO, multi-tasking, and memory management.
Prerequisites

- Minimum grade of C or better - in
  - CMSC330, and
  - CMSC351
- 1 course with a minimum grade of C- from
  - CMSC414,
  - CMSC417,
  - CMSC420,
  - CMSC430,
  - CMSC433,
  - CMSC435,
  - ENEE440,
  - ENEE457
Class Overview

• Class Web Page

• Piazza
  – https://piazza.com/class/jzcrm1vz6j064b
<table>
<thead>
<tr>
<th>Week</th>
<th>Readings</th>
<th>Readings</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Chapter 1</td>
<td>Chapter 1</td>
</tr>
<tr>
<td></td>
<td>Monday, August 26, 2019</td>
<td>Project Setup</td>
</tr>
<tr>
<td></td>
<td>Tuesday, August 27, 2019</td>
<td>Course Overview</td>
</tr>
<tr>
<td></td>
<td>Wednesday, August 28, 2019</td>
<td>Project Setup</td>
</tr>
<tr>
<td></td>
<td>Thursday, August 29, 2019</td>
<td>Course Overview</td>
</tr>
<tr>
<td></td>
<td>Friday, August 30, 2019</td>
<td>Project Z Due Mon Feb 4</td>
</tr>
<tr>
<td>2</td>
<td>Monday, September 2, 2019</td>
<td>Holiday</td>
</tr>
<tr>
<td></td>
<td>Tuesday, September 3, 2019</td>
<td>Geek OS Slides</td>
</tr>
<tr>
<td></td>
<td>Wednesday, September 4, 2019</td>
<td>GeekOS debugging. Proj 0 intro (file descriptor, pipe, etc.)</td>
</tr>
<tr>
<td></td>
<td>Thursday, September 5, 2019</td>
<td>Geek OS Slides</td>
</tr>
<tr>
<td></td>
<td>Friday, September 6, 2019</td>
<td>Project 0 (pipe) Due Mon Feb 11</td>
</tr>
<tr>
<td>3</td>
<td>Monday, September 9, 2019</td>
<td>OS Structures</td>
</tr>
<tr>
<td></td>
<td>Tuesday, September 10, 2019</td>
<td>Introduction to Processes</td>
</tr>
<tr>
<td></td>
<td>Wednesday, September 11, 2019</td>
<td>Introduction to Processes</td>
</tr>
<tr>
<td></td>
<td>Thursday, September 12, 2019</td>
<td>Introduction to Processes</td>
</tr>
<tr>
<td></td>
<td>Friday, September 13, 2019</td>
<td>Introduction to Processes</td>
</tr>
<tr>
<td>4</td>
<td>Monday, September 16, 2019</td>
<td>Processes and Threads</td>
</tr>
<tr>
<td></td>
<td>Tuesday, September 17, 2019</td>
<td>Processes and Threads</td>
</tr>
<tr>
<td></td>
<td>Wednesday, September 18, 2019</td>
<td>Processes and Threads</td>
</tr>
<tr>
<td></td>
<td>Thursday, September 19, 2019</td>
<td>Processes and Threads</td>
</tr>
<tr>
<td></td>
<td>Friday, September 20, 2019</td>
<td>Processes and Threads</td>
</tr>
<tr>
<td>5</td>
<td>Monday, September 23, 2019</td>
<td>Chapter 4 6 and 7 Concurrency</td>
</tr>
<tr>
<td></td>
<td>Tuesday, September 24, 2019</td>
<td>Chapter 4 6 and 7 Concurrency</td>
</tr>
<tr>
<td></td>
<td>Wednesday, September 25, 2019</td>
<td>Chapter 4 6 and 7 Concurrency</td>
</tr>
<tr>
<td></td>
<td>Thursday, September 26, 2019</td>
<td>Chapter 4 6 and 7 Concurrency</td>
</tr>
<tr>
<td></td>
<td>Friday, September 27, 2019</td>
<td>Chapter 4 6 and 7 Concurrency</td>
</tr>
<tr>
<td>6</td>
<td>Monday, September 30, 2019</td>
<td>OSTEP: Chapters 26-28, 30-32 (Concurrency)</td>
</tr>
<tr>
<td></td>
<td>Tuesday, October 1, 2019</td>
<td>Synchronization Examples</td>
</tr>
<tr>
<td></td>
<td>Wednesday, October 2, 2019</td>
<td>EXAM 1</td>
</tr>
<tr>
<td></td>
<td>Thursday, October 3, 2019</td>
<td>Project 2(Signals)</td>
</tr>
<tr>
<td>7</td>
<td>Monday, October 7, 2019</td>
<td>Chapter 5 CPU scheduling</td>
</tr>
<tr>
<td></td>
<td>Tuesday, October 8, 2019</td>
<td>CPU Scheduling</td>
</tr>
<tr>
<td></td>
<td>Wednesday, October 9, 2019</td>
<td>CPU Scheduling</td>
</tr>
<tr>
<td></td>
<td>Thursday, October 10, 2019</td>
<td>CPU Scheduling</td>
</tr>
<tr>
<td></td>
<td>Friday, October 11, 2019</td>
<td>Project 3 (per-cpu variables)</td>
</tr>
<tr>
<td>8</td>
<td>Monday, October 14, 2019</td>
<td>Chapter 5 CPU Scheduling and 9 Memory Management</td>
</tr>
<tr>
<td></td>
<td>Tuesday, October 15, 2019</td>
<td>CPU Scheduling</td>
</tr>
<tr>
<td></td>
<td>Wednesday, October 16, 2019</td>
<td>CPU Scheduling</td>
</tr>
<tr>
<td></td>
<td>Thursday, October 17, 2019</td>
<td>CPU Scheduling</td>
</tr>
<tr>
<td></td>
<td>Friday, October 18, 2019</td>
<td>CPU Scheduling</td>
</tr>
<tr>
<td>9</td>
<td>Monday, October 21, 2019</td>
<td>Final Exam 1:30 to 3:30</td>
</tr>
</tbody>
</table>
Text

• Required
    Siberschatz, Galvin and Gagne,
  – John Wiley 2018

• Available
  – [https://www.wiley.com](https://www.wiley.com) – E-Book $ 76.00
    • May rent at lower price
  – [https://hubetext.com/shop](https://hubetext.com/shop) - PDF $8.00
Programming Projects:

• Understanding operating system concepts is a hands-on activity. This class will include several substantial programming projects that will require students to read and understand provided code, write new modules, and debug the resulting system. The programming assignments will be time consuming and students taking this class should plan their class schedules accordingly.

• The instructor reserves the right to fail, regardless of overall numeric score, students who do not submit a good faith attempt to complete all programming assignments.
Grading

• Final Exam 25%
• Midterms (2 each worth 15%) 30%
• Programming Assignments 43%
• Class Participation 2%

Exams:
• Midterm #1
• Midterm #2
• Final
Class Grades Server
http://grades.cs.umd.edu

• Complete grade information
• Interface for requesting regrades on exams and projects
Some Useful Videos
By Dr. Neil Spring

• Review of 216
  – Sizes - Necessary distinction between sizeof and strlen.
  – Malloc - Model for how malloc tracks memory, how to interpret memory errors.
  – Timing - Reminder of user / kernel separation.

• Synchronization Topics
  – Synchronization Overview - The basics
  – Semaphore Interface - How Semaphores can be used.
  – Semaphore Implementation - How Semaphores are built (so you know what they are and don't reinvent them).

• Would require UMD CAS for Box Access
Computer System
Environment

- CPU executes machine instructions
  - Fetches from memory – Machine Instruction
    - PC - incremented after fetching
  - Decodes
  - Fetches operands – as required
    - From memory
    - In Registers
  - Executes
    - Saves results
      - In Registers
      - Memory
Environment

- CPU executes machine instructions
  - Fetches from memory – Machine Instruction
    - PC - incremented after fetching
  - Decodes
  - Fetches operands – as required
    - From memory
    - In Registers
  - Executes
    - Saves results
      - In Registers
      - Memory

What happens if something goes wrong??
Nothing happens unless provisions are made for it,
Detection
Action
In hardware
In software
Detection

• Must be by Hardware
  – Why??

• Action
  – Hardware??
  – Software??
Hardware Provisions

• Privileged Instructions
• Machine Modes
  – User Mode
  – Kernel Mode
• PS Register
• Means of switching modes
  – Hardware support
Uses of Kernel Mode

• Running OS Code
• Management Functions
  – Processor
  – Memory
  – I/O
• Error Handling
• Protection
Chapter 1: Introduction
Chapter 1: Introduction

• What Operating Systems Do
• Computer-System Organization
• Computer-System Architecture
• Operating-System Structure
• Operating-System Operations
• Process Management
• Memory Management
• Storage Management
• Protection and Security
• Kernel Data Structures
• Computing Environments
• Open-Source Operating Systems
Objectives

• To describe the basic organization of computer systems
• To provide a grand tour of the major components of operating systems
• To give an overview of the many types of computing environments
• To explore several open-source operating systems
Four Components of a Computer System

- User 1
- User 2
- User 3
- User n

- Compiler
- Assembler
- Text Editor
- Database System

- System and Application Programs

- Operating System

- Computer Hardware
Computer System Structure

- Computer system can be divided into four components:
  - Hardware – provides basic computing resources
    - CPU, memory, I/O devices
  - Operating system
    - Controls and coordinates use of hardware among various applications and users
  - Application programs – define the ways in which the system resources are used to solve the computing problems of the users
    - Word processors, compilers, web browsers, database systems, video games
  - Users
    - People, machines, other computers
What is an Operating System?

- A program that acts as an intermediary between a user of a computer and the computer hardware

- Operating system goals:
  - Execute user programs and make solving user problems easier
  - Make the computer system convenient to use
  - Use the computer hardware in an efficient manner
What Operating Systems Do

• Depends on the point of view
• Users want convenience, **ease of use** and **good performance**
  – Don’t care about **resource utilization**
• But shared computer such as **mainframe** or **servers** must keep all users happy
• Users of dedicate systems such as **workstations** have dedicated resources but frequently use shared resources from **servers**
• Handheld computers are resource poor, optimized for usability and battery life
• Some computers have little or no user interface, such as embedded computers in devices and automobiles
Operating System Definition

• OS is a **resource allocator**
  – Manages all resources
  – Decides between conflicting requests for efficient and fair resource use

• OS is a **control program**
  – Controls execution of programs to prevent errors and improper use of the computer

Copyright 2018 Silberschatz, Gavin & Gagne
Operating System Definition (Cont.)

• No universally accepted definition
• “Everything a vendor ships when you order an operating system” is a good approximation
  – But varies wildly
• “The one program running at all times on the computer” is the kernel.
• Everything else is either
  – a system program (ships with the operating system), or
  – an application program.
Computer Startup

• **bootstrap program** is loaded at power-up or reboot
  - Typically stored in ROM or EPROM, generally known as **firmware**
  - Initializes all aspects of system
  - Loads operating system kernel and starts execution
Computer System Organization

- Computer-system operation
  - One or more CPUs, device controllers connect through common bus providing access to shared memory
  - Concurrent execution of CPUs and devices competing for memory cycles
Computer-System Operation

- I/O devices and the CPU can execute concurrently
- Each device controller is in charge of a particular device type
- Each device controller has a local buffer
- CPU moves data from/to main memory to/from local buffers
- I/O is from the device to local buffer of controller
- Device controller informs CPU that it has finished its operation by causing an interrupt
Common Functions of Interrupts

- Interrupt transfers control to the interrupt service routine generally, through the interrupt vector, which contains the addresses of all the service routines.
- Interrupt architecture must save the address of the interrupted instruction.
- A trap or exception is a software-generated interrupt caused either by an error or a user request.
- An operating system is interrupt driven.
Interrupt Handling

• The operating system preserves the state of the CPU by storing registers and the program counter

• Determines which type of interrupt has occurred:
  – polling
  – vectored interrupt system

• Separate segments of code determine what action should be taken for each type of interrupt
Interrupt Timeline

CPU
user process executing

I/O interrupt processing

I/O request transfer done I/O request transfer done

I/O device
idle transferring
I/O Structure

• After I/O starts, control returns to user program only upon I/O completion
  – Wait instruction idles the CPU until the next interrupt
  – Wait loop (contention for memory access)
  – At most one I/O request is outstanding at a time, no simultaneous I/O processing

• After I/O starts, control returns to user program without waiting for I/O completion
  – System call – request to the OS to allow user to wait for I/O completion
  – Device-status table contains entry for each I/O device indicating its type, address, and state
  – OS indexes into I/O device table to determine device status and to modify table entry to include interrupt
The basic unit of computer storage is the **bit**. A bit can contain one of two values, 0 and 1. All other storage in a computer is based on collections of bits. Given enough bits, it is amazing how many things a computer can represent: numbers, letters, images, movies, sounds, documents, and programs, to name a few. A **byte** is 8 bits, and on most computers it is the smallest convenient chunk of storage. For example, most computers don’t have an instruction to move a bit but do have one to move a byte. A less common term is **word**, which is a given computer architecture’s native unit of data. A word is made up of one or more bytes. For example, a computer that has 64-bit registers and 64-bit memory addressing typically has 64-bit (8-byte) words. A computer executes many operations in its native word size rather than a byte at a time.

Computer storage, along with most computer throughput, is generally measured and manipulated in bytes and collections of bytes.  
A **kilobyte**, or **KB**, is 1,024 bytes  
a **megabyte**, or **MB**, is $1,024^2$ bytes  
a **gigabyte**, or **GB**, is $1,024^3$ bytes  
a **terabyte**, or **TB**, is $1,024^4$ bytes  
a **petabyte**, or **PB**, is $1,024^5$ bytes

Computer manufacturers often round off these numbers and say that a megabyte is 1 million bytes and a gigabyte is 1 billion bytes. Networking measurements are an exception to this general rule; they are given in bits (because networks move data a bit at a time).
Storage Structure

• Main memory – only large storage media that the CPU can access directly
  – Random access
  – Typically volatile

• Secondary storage – extension of main memory that provides large nonvolatile storage capacity

• Hard disks – rigid metal or glass platters covered with magnetic recording material
  – Disk surface is logically divided into tracks, which are subdivided into sectors
  – The disk controller determines the logical interaction between the device and the computer

• **Solid-state disks** – faster than hard disks, nonvolatile
  – Various technologies
  – Becoming more popular
Storage Hierarchy

• Storage systems organized in hierarchy
  – Speed
  – Cost
  – Volatility

• **Caching** – copying information into faster storage system; main memory can be viewed as a cache for secondary storage

• **Device Driver** for each device controller to manage I/O
  – Provides uniform interface between controller and kernel
Storage-Device Hierarchy
Caching

• Important principle, performed at many levels in a computer (in hardware, operating system, software)
• Information in use copied from slower to faster storage temporarily
• Faster storage (cache) checked first to determine if information is there
  – If it is, information used directly from the cache (fast)
  – If not, data copied to cache and used there
• Cache smaller than storage being cached
  – Cache management important design problem
  – Cache size and replacement policy
Direct Memory Access Structure

- Used for high-speed I/O devices able to transmit information at close to memory speeds
- Device controller transfers blocks of data from buffer storage directly to main memory without CPU intervention
- Only one interrupt is generated per block, rather than the one interrupt per byte
How a Modern Computer Works

A von Neumann architecture

Copyright 2018 Silberschatz, Gavin & Gagne
Operating System Structure

• **Multiprogramming** *(Batch system)* needed for efficiency
  - Single user cannot keep CPU and I/O devices busy at all times
  - Multiprogramming organizes jobs (code and data) so CPU always has one to execute
  - A subset of total jobs in system is kept in memory
  - One job selected and run via **job scheduling**
  - When it has to wait (for I/O for example), OS switches to another job

• **Timesharing** *(multitasking)* is logical extension in which CPU switches jobs so frequently that users can interact with each job while it is running, creating **interactive** computing
  - **Response time** should be < 1 second
  - Each user has at least one program executing in memory ⇒ **process**
  - If several jobs ready to run at the same time ⇒ **CPU scheduling**
  - If processes don’t fit in memory, **swapping** moves them in and out to run
  - **Virtual memory** allows execution of processes not completely in memory
Memory Layout for Multiprogrammed System

0
operating system

512M
job 1

job 2

job 3

job 4

Copyright 2018 Silberschatz, Gavin & Gagne
Operating-System Operations

• **Interrupt driven** (hardware and software)
  – Hardware interrupt by one of the devices
  – Software interrupt (**exception** or **trap**):
    • Software error (e.g., division by zero)
    • Request for operating system service
    • Other process problems include infinite loop, processes modifying each other or the operating system
Operating-System Operations (cont.)

- **Dual-mode** operation allows OS to protect itself and other system components
  - **User mode** and **kernel mode**
  - **Mode bit** provided by hardware
    - Provides ability to distinguish when system is running user code or kernel code
    - Some instructions designated as **privileged**, only executable in kernel mode
    - System call changes mode to kernel, return from call resets it to user

- Increasingly CPUs support multi-mode operations
  - i.e. **virtual machine manager (VMM)** mode for guest **VMs**
Transition from User to Kernel Mode

- Timer to prevent infinite loop / process hogging resources
  - Timer is set to interrupt the computer after some time period
  - Keep a counter that is decremented by the physical clock.
  - Operating system set the counter (privileged instruction)
  - When counter zero generate an interrupt
  - Set up before scheduling process to regain control or terminate program that exceeds allotted time
Process Management

• A process is a program in execution. It is a unit of work within the system. Program is a **passive entity**, process is an **active entity**.
• Process needs resources to accomplish its task
  – CPU, memory, I/O, files
  – Initialization data
• Process termination requires reclaim of any reusable resources
• Single-threaded process has one **program counter** specifying location of next instruction to execute
  – Process executes instructions sequentially, one at a time, until completion
• Multi-threaded process has one program counter per thread
• Typically system has many processes, some user, some operating system running concurrently on one or more CPUs
  – Concurrency by multiplexing the CPUs among the processes / threads
Process Management Activities

The operating system is responsible for the following activities in connection with process management:

- Creating and deleting both user and system processes
- Suspending and resuming processes
- Providing mechanisms for process synchronization
- Providing mechanisms for process communication
- Providing mechanisms for deadlock handling
Memory Management

• To execute a program all (or part) of the instructions must be in memory
• All (or part) of the data that is needed by the program must be in memory.
• Memory management determines what is in memory and when
  — Optimizing CPU utilization and computer response to users
• Memory management activities
  — Keeping track of which parts of memory are currently being used and by whom
  — Deciding which processes (or parts thereof) and data to move into and out of memory
  — Allocating and deallocating memory space as needed
Storage Management

• OS provides uniform, logical view of information storage
  – Abstracts physical properties to logical storage unit
  – Each medium is controlled by device (i.e., disk drive, tape drive)
    • Varying properties include access speed, capacity, data-transfer rate, access method (sequential or random)

• File-System management
  – Files usually organized into directories
  – Access control on most systems to determine who can access what
  – OS activities include
    • Creating and deleting files and directories
    • Primitives to manipulate files and directories
    • Mapping files onto secondary storage
    • Backup files onto stable (non-volatile) storage media
Mass-Storage Management

- Usually disks used to store data that does not fit in main memory or data that must be kept for a “long” period of time
- Proper management is of central importance
- Entire speed of computer operation hinges on disk subsystem and its algorithms
- OS activities
  - Free-space management
  - Storage allocation
  - Disk scheduling
- Some storage need not be fast
  - Tertiary storage includes optical storage, magnetic tape
  - Still must be managed – by OS or applications
  - Varies between WORM (write-once, read-many-times) and RW (read-write)
Performance of Various Levels of Storage

<table>
<thead>
<tr>
<th>Level</th>
<th>1</th>
<th>2</th>
<th>3</th>
<th>4</th>
<th>5</th>
</tr>
</thead>
<tbody>
<tr>
<td>Name</td>
<td>registers</td>
<td>cache</td>
<td>main memory</td>
<td>solid state disk</td>
<td>magnetic disk</td>
</tr>
<tr>
<td>Typical size</td>
<td>&lt; 1 KB</td>
<td>&lt; 16MB</td>
<td>&lt; 64GB</td>
<td>&lt; 1 TB</td>
<td>&lt; 10 TB</td>
</tr>
<tr>
<td>Implementation technology</td>
<td>custom memory with multiple ports CMOS</td>
<td>on-chip or off-chip CMOS SRAM</td>
<td>CMOS SRAM</td>
<td>flash memory</td>
<td>magnetic disk</td>
</tr>
<tr>
<td>Access time (ns)</td>
<td>0.25 - 0.5</td>
<td>0.5 - 25</td>
<td>80 - 250</td>
<td>25,000 - 50,000</td>
<td>5,000,000</td>
</tr>
<tr>
<td>Bandwidth (MB/sec)</td>
<td>20,000 - 100,000</td>
<td>5,000 - 10,000</td>
<td>1,000 - 5,000</td>
<td>500</td>
<td>20 - 150</td>
</tr>
<tr>
<td>Managed by</td>
<td>compiler</td>
<td>hardware</td>
<td>operating system</td>
<td>operating system</td>
<td>operating system</td>
</tr>
<tr>
<td>Backed by</td>
<td>cache</td>
<td>main memory</td>
<td>disk</td>
<td>disk</td>
<td>disk or tape</td>
</tr>
</tbody>
</table>

Movement between levels of storage hierarchy can be explicit or implicit.
Migration of data “A” from Disk to Register

- Multitasking environments must be careful to use most recent value, no matter where it is stored in the storage hierarchy

- Multiprocessor environment must provide cache coherency in hardware such that all CPUs have the most recent value in their cache

- Distributed environment situation even more complex
  - Several copies of a datum can exist
  - Various solutions covered in Chapter 17
I/O Subsystem

- One purpose of OS is to hide peculiarities of hardware devices from the user
- I/O subsystem responsible for
  - Memory management of I/O including
    - buffering (storing data temporarily while it is being transferred),
    - caching (storing parts of data in faster storage for performance),
    - spooling (the overlapping of output of one job with input of other jobs)
  - General device-driver interface
  - Drivers for specific hardware devices
I/O Structure

• Storage is one of many types of I/O devices
• Each device connected to a controller
  – Some controllers provide a bus for one or more devices (i.e. SCSI)
  – Device driver for each device controller
    • Knows details of controller
    • Provides uniform interface to kernel

• I/O operation
  – Device driver loads controller registers appropriately
  – Controller examines registers, executes I/O
  – Controller interrupts to signal device driver that I/O completed
  – High overhead for moving bulk data (i.e. disk I/O)

• Direct Memory Access (DMA)
  – Device controller transfers block of data to/from main memory
  – Interrupts when block transfer completed
I/O Structure

• After I/O starts, control returns to user program only upon I/O completion.
  – Wait instruction idles the CPU until the next interrupt
  – Wait loop (contention for memory access).
  – At most one I/O request is outstanding at a time, no simultaneous I/O processing.

• After I/O starts, control returns to user program without waiting for I/O completion.
  – *System call* – request to the operating system to allow user to wait for I/O completion.
  – *Device-status table* contains entry for each I/O device indicating its type, address, and state.
  – Operating system indexes into I/O device table to determine device status and to modify table entry to include interrupt.
Two I/O Methods

Synchronous

Asynchronous

Copyright 2018 Silberschatz, Gavin & Gagne
## Device-Status Table

<table>
<thead>
<tr>
<th>Device</th>
<th>Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>card reader 1</td>
<td>idle</td>
</tr>
<tr>
<td>line printer 3</td>
<td>busy</td>
</tr>
<tr>
<td>disk unit 1</td>
<td>idle</td>
</tr>
<tr>
<td>disk unit 2</td>
<td>idle</td>
</tr>
<tr>
<td>disk unit 3</td>
<td>busy</td>
</tr>
</tbody>
</table>

- **Request for line printer**
  - Address: 38546
  - Length: 1372

- **Request for disk unit 3**
  - File: xxx
  - Operation: read
  - Address: 43046
  - Length: 20000

- **Request for disk unit 3**
  - File: yyy
  - Operation: write
  - Address: 03458
  - Length: 500
Direct Memory Access Structure

• Used for high-speed I/O devices able to transmit information at close to memory speeds.
• Device controller transfers blocks of data from buffer storage directly to main memory without CPU intervention.
• Only on interrupt is generated per block, rather than the one interrupt per byte.
Protection and Security

- **Protection** – any mechanism for controlling access of processes or users to resources defined by the OS
- **Security** – defense of the system against internal and external attacks
  - Huge range, including denial-of-service, worms, viruses, identity theft, theft of service
- Systems generally first distinguish among users, to determine who can do what
  - User identities (**user IDs**, security IDs) include name and associated number, one per user
  - User ID then associated with all files, processes of that user to determine access control
  - Group identifier (**group ID**) allows set of users to be defined and controls managed, then also associated with each process, file
  - **Privilege escalation** allows user to change to effective ID with more rights
Computing Environments - Traditional

- Stand-alone general purpose machines
- But blurred as most systems interconnect with others (i.e., the Internet)
- **Portals** provide web access to internal systems
- **Network computers** *(thin clients)* are like Web terminals
- Mobile computers interconnect via **wireless networks**
- Networking becoming ubiquitous – even home systems use **firewalls** to protect home computers from Internet attacks
Computing Environments - Mobile

• Handheld smartphones, tablets, etc
• What is the functional difference between them and a “traditional” laptop?
• Extra feature – more OS features (GPS, gyroscope)
• Allows new types of apps like augmented reality
• Use IEEE 802.11 wireless, or cellular data networks for connectivity
• Leaders are Apple iOS and Google Android
Computing Environments – Distributed

• Distributed computing
  – Collection of separate, possibly heterogeneous, systems networked together
    • **Network** is a communications path, **TCP/IP** most common
      – **Local Area Network** (LAN)
      – **Wide Area Network** (WAN)
      – **Metropolitan Area Network** (MAN)
      – **Personal Area Network** (PAN)
    – **Network Operating System** provides features between systems across network
      • Communication scheme allows systems to exchange messages
      • Illusion of a single system
Computing Environments – Client-Server

- Client-Server Computing
  - Dumb terminals supplanted by smart PCs
  - Many systems now servers, responding to requests generated by clients
    - Compute-server system provides an interface to client to request services (i.e., database)
    - File-server system provides interface for clients to store and retrieve files
• Another model of distributed system
• P2P does not distinguish clients and servers
  – Instead all nodes are considered peers
  – May each act as client, server or both
  – Node must join P2P network
    • Registers its service with central lookup service on network, or
    • Broadcast request for service and respond to requests for service via *discovery protocol*
  – Examples include Napster and Gnutella, *Voice over IP (VoIP)* such as Skype
Computing Environments - Virtualization

- Allows operating systems to run applications within other OSes
  - Vast and growing industry

- **Emulation** used when source CPU type different from target type (i.e. PowerPC to Intel x86)
  - Generally slowest method
  - When computer language not compiled to native code – **Interpretation**

- **Virtualization** – OS natively compiled for CPU, running guest OSes also natively compiled
  - Consider VMware running WinXP guests, each running applications, all on native WinXP **host** OS
  - **VMM** (virtual machine Manager) provides virtualization services
Computing Environments - Virtualization

• Use cases involve laptops and desktops running multiple OSes for exploration or compatibility
  – Apple laptop running Mac OS X host, Windows as a guest
  – Developing apps for multiple OSes without having multiple systems
  – QA testing applications without having multiple systems
  – Executing and managing compute environments within data centers

• VMM can run natively, in which case they are also the host
  – There is no general purpose host then (VMware ESX and Citrix XenServer)
Computing Environments - Virtualization

(a) Non-virtualized system

- processes
- kernel
- hardware

(b) Virtualized system

- processes
- kernel
- virtual machine manager
- hardware

Copyright 2018 Silberschatz, Gavin & Gagne
Computing Environments – Cloud Computing

• Delivers computing, storage, even apps as a service across a network
• Logical extension of virtualization because it uses virtualization as the base for it functionality.
  – Amazon EC2 has thousands of servers, millions of virtual machines, petabytes of storage available across the Internet, pay based on usage
• Many types
  – Public cloud – available via Internet to anyone willing to pay
  – Private cloud – run by a company for the company’s own use
  – Hybrid cloud – includes both public and private cloud components
  – Software as a Service (SaaS) – one or more applications available via the Internet (i.e., word processor)
  – Platform as a Service (PaaS) – software stack ready for application use via the Internet (i.e., a database server)
  – Infrastructure as a Service (IaaS) – servers or storage available over Internet (i.e., storage available for backup use)
• Cloud computing environments composed of traditional OSes, plus VMMs, plus cloud management tools
  – Internet connectivity requires security like firewalls
  – Load balancers spread traffic across multiple applications
Computing Environments – Real-Time Embedded Systems

• Real-time embedded systems most prevalent form of computers
  – Vary considerable, special purpose, limited purpose OS, real-time OS
  – Use expanding
• Many other special computing environments as well
  – Some have OSes, some perform tasks without an OS
• Real-time OS has well-defined fixed time constraints
  – Processing must be done within constraint
  – Correct operation only if constraints met
Open-Source Operating Systems

• Operating systems made available in source-code format rather than just binary closed-source
• Counter to the copy protection and Digital Rights Management (DRM) movement
• Started by Free Software Foundation (FSF), which has “copyleft” GNU Public License (GPL)
• Examples include GNU/Linux and BSD UNIX (including core of Mac OS X), and many more
• Can use VMM like VMware Player (Free on Windows), Virtualbox (open source and free on many platforms - http://www.virtualbox.com)
  – Use to run guest operating systems for exploration
End of Chapter 1