

A Single-Copy Distributed Shared Memory

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Single-Copy Distributed Shared Memory Implementation

Proving the implements conditions

- Program **DsmSCopyDist**:
 - implements **coherent dsm** using **object-transfer service**
 - starts obj-transfer service over given addrs
 - starts **DsmSCopy** system at every addr of obj-transfer service
- Each DsmSCopy system holds a subset of pages for its user
- Initially, pages are partitioned amongst DsmSCopy systems
- When user attempts access to a page
 - if page is not local, use obj-transfer service to obtain page
 - if page is local, access page as usual

- program `DsmSCopyDist ( ADDR, PNO, PVAL, {initPagesj} )`
// `initPagesj`: initial page numbers at addr `j`
 $\{w_j\} \leftarrow \text{start } \text{ObjTransferService} (\text{ADDR}, \text{PNO}, \text{PVAL}, \text{initPages})$
 for `j` in `ADDR`
 $v_j \leftarrow \text{start } \text{DsmSCopy} (\text{PNO}, \text{PVAL}, \text{initPages}_j, w_j)$
 return $\{v_j\}$
- Input fns: `vj.read(pn)`, `vj.write(pn, pv)`
- Implements `DsmSeqConService ( ADDR, PNO, PVAL )`

- Parameters: PNO, PVAL, initPages_j, w_j // **note**: no addr
- Input fns: **mysid.read(pn)**, **mysid.write(pn, pv)**
- Local fns: **doRxReq()** // rcvs reqs from obj-xfr service
- Output calls: **w_j.acq(pn)**, **w_j.rel(pn, pv)**, **w_j.rxReq()**
- Main
 - pgVal: map <PNO, PVAL> // pn-entry iff page pn local
pgVal_{pn} ← 0, for pn in initPages_j
 - startThread (doRxReq())

```
■ input mysid.read(pn):  
  await (true)  
  if (pgVal has pn entry)  
    return pgValpn  
● tpv ← wj.acq(pn)  
  await (true)  
  pgValpn ← tpv  
  return pgValpn
```

- input mysid.write(pn, pv):
 - await (true)
 - if (pgVal has pn entry)
 - pgVal_{pn} ← pv
 - return
- tpv ← w_j.acq(pn)
 - await (true)
 - pgVal_{pn} ← tpv
 - pgVal_{pn} ← pv
 - return

- function doRxReq():
 - while (true)
 - $pn \leftarrow w_j.rxReq()$
 - if { pn in PNO }
 - await (pn -entry in $pgVal$)
 - $w_j.rel(pn, pgVal_{pn})$
 - remove pn -entry from $pgVal$
- atomicity assumption: await
- progress assumption: weak fairness for all threads

Single-Copy Distributed Shared Memory Implementation

Proving the implements conditions

- To prove: `DsmSCopyDist` implements `DsmSeqConService`
- Define program Z of implementation and service inverse si
- Identify effective atomicity breakpoints // ●
- Identify assertions A to hold
- Prove Z satisfies A

Single-Copy Distributed Shared Memory Implementation

Proving the implements conditions

- Program Z of implementation and service inverse

- Safety analysis

- Progress analysis

- Parameters:

- ADDR, PNO, PVAL, initPages

- DsmSCopy at j:

j.pgVal

startThread (doRxReq())

- DsmSeqConServiceInverse:

ic { ... }

$\alpha \leftarrow []$ // wcalls, rrets

```
■ input j.read(pn):  
    await (true)  
    if (pgValpn exists)  
b1:      return pgValpn  
    • tpv ← wj.acq(pn)  
    await (true)  
    pgValpn ← tpv  
b2:      return pgValpn
```

```
■ doRead(j,pn)  
    oc { no ongoing  
        j.read(.) or j.write(.)  
    }  
    pv ← j.read(pn)  
    ic { seqConsistent(  
         $\alpha \circ [[\text{RRET}, j, pn, pv]]$ ) }  
     $\alpha$ .append( $[[\text{RRET}, j, pn, pv]]$ )
```

```
■ input j.write(pn, pv):  
    await (true)  
    if (pgValpn exists)  
        pgValpn ← pv  
b3:    return  
b4: ● tpv ← wj.acq(pn)  
    await (true)  
    pgValpn ← tpv  
    pgValpn ← pv  
b5:    return
```

```
■ doWrite(j, pn)  
    oc { no ongoing  
        j.read(.) or j.write(.)  
         $\alpha$ .append([[WCALL, j, pn, pv]])}  
    j.write(pn, pv)  
    ic { true }
```

- | | |
|-----------------------------|-----------------------------|
| ■ function doRxReq(): ... | |
| ■ atomicity assumption: ... | ■ atomicity assumption: ... |
| ■ progress assumption: ... | ■ progress condition: ... |
-
- ObjTransferService (ADDR, PNO, PVAL, initPages)

Single-Copy Distributed Shared Memory Implementation

Proving the implements conditions

Program Z of implementation and service inverse

Safety analysis

Progress analysis

- To satisfy service, need to prove Z satisfies A_1

$A_1 : \text{Inv thread at si.doRead}(j, pn).ic$
 $\Rightarrow \text{seqConsistent}(\alpha \circ [[\text{RRET}, j, pn, pv]])$

- A_1 equivalent to $\text{Inv } B_1$ // wcall preserves seq consistency

$B_1 : \text{seqConsistent}(\alpha)$

- Define auxiliary var β st Z satisfies $Inv\ B_2-B_3$

B_2 : forall(j : [$.., j, ..$] in β] = [$.., j, ..$] in α))

B_3 : sequential(β)

- Candidate: $\beta = \beta_S \circ \beta_T$ // stable $\circ$ transient
 - when [$.., j, pn, pv$] is appended to α
 - append it to β_S if $j.pgVal_{pn}$ exists // at **b1, b2, b3**
 - append it to β_T o/w // at **b4**
 - when $j.write(pn, pv)$ call becomes unblocked
 - remove its entry from β_T and append to β_S // at **b5**
- Sufficient to prove $Inv\ B_2-B_3$

- Requires proving obj-xfr service is used correctly // o/w fault

B_4 : thread at $w_j.acq(pn) \Rightarrow$ no ongoing $w_j.acq(pn)$

B_5 : thread at $w_j.rel(pn, pv) \Rightarrow (pn \text{ in } w.objs_j \text{ and in } w.reqs_j)$

B_6 : thread at $w_j.rxReq() \Rightarrow$ no ongoing $w_j.rxReq()$

- Proving $Inv\ B_2-B_6$ is straightforward
 - proving $Inv\ B_4-B_6$ is trivial
 - some helpful predicates are given next

- Possibly helpful predicates in proving $Inv\ B_2-B_6$

B_7 : forall pn, exactly one of the following hold:

- forone(j: pgVal_{pn} exists)
- forall(j: no pgVal_{pn} exists) and (w.val_{pn} exists)

B_8 : val_{pn} exists $\Rightarrow$ val_{pn} = lastWrite(β_S ,pn)

B_9 : j.pgVal_{pn} exists $\Rightarrow$ val_{pn} = lastWrite(β_S ,pn)

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Proving the implements conditions

Program Z of implementation and service inverse

Safety analysis

Progress analysis

- Obj-xfr service progress holds because it is used correctly
- Given this, proof of A_2 and A_3 is straightforward
- See text for details

- To satisfy service, need to prove Z satisfies A_1 – A_2

A_2 : ongoing $j.read(.)$ *leads-to* not ongoing $j.read(.)$

A_3 : ongoing $j.write(.)$ *leads-to* not ongoing $j.write(.)$

- Obj-xfr service progress holds // coz Z uses service correctly

P_3 : ongoing $w_j.\text{rel}(\cdot)$ *leads-to* no ongoing $w_j.\text{rel}(\cdot)$

P_4 : ($\dots$ *leads-to* $\dots$) $\Rightarrow$
($\text{pn} \in w.\text{objs}_j$ & ongoing $w_k.\text{acq}(\text{pn})$) *leads-to* $\text{pn} \in w.\text{reqs}_j$

P_5 : ($\dots$ *leads-to* $\dots$) $\Rightarrow$
ongoing $w_j.\text{acq}(\text{pn})$ *leads-to* no ongoing $w_j.\text{acq}(\text{pn})$

- Thread in $j.\text{doRxReq}()$ always waits for a req and releases the requested page

P_2 : pn in $w.\text{reqs}_j$ *leads-to* not pn in $w.\text{reqs}_j$

- P_2 implies P_3 – P_5 .lhs.

- P_3 – P_5 .rhs implies A_2 – A_3